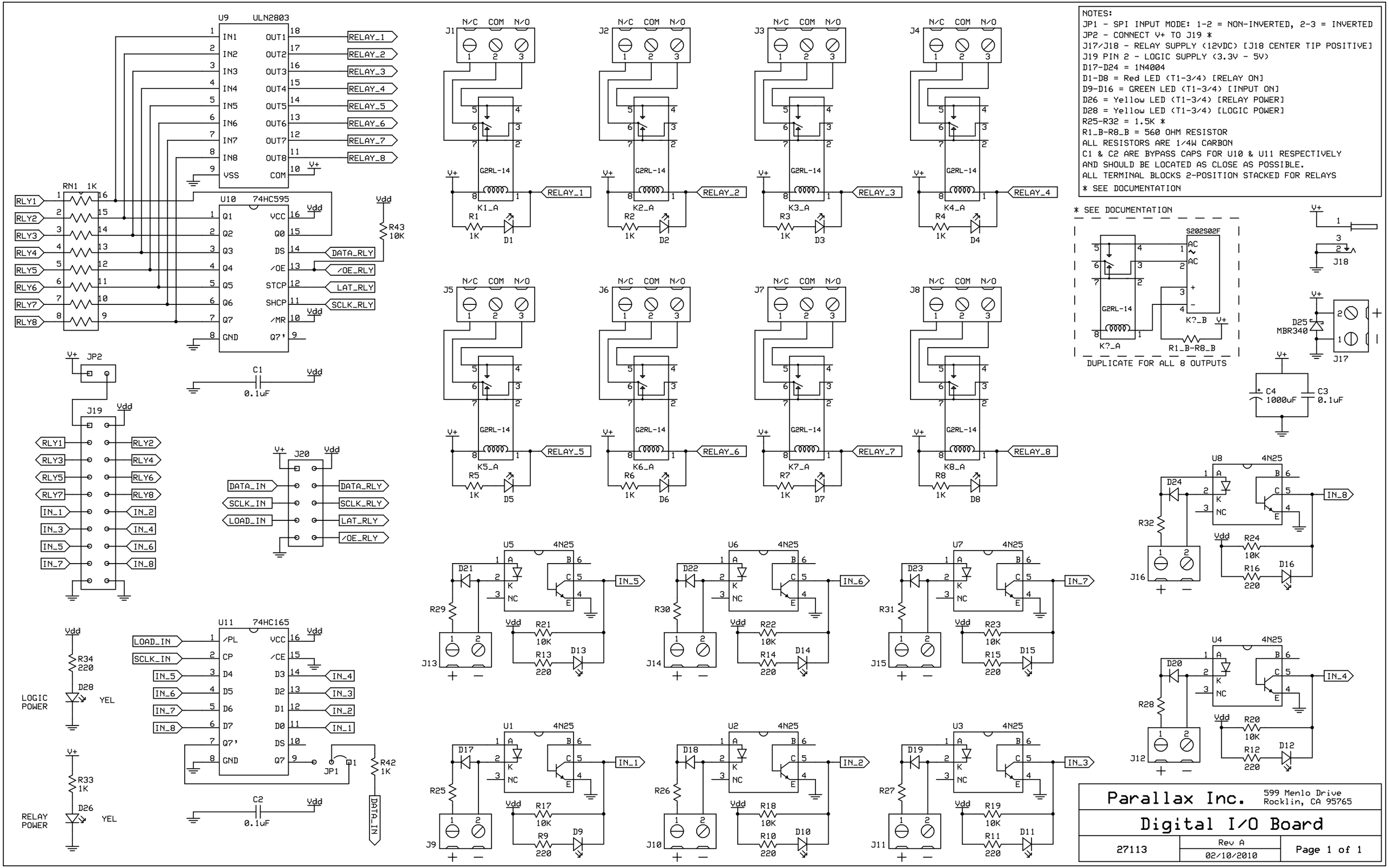
NOTES:
JP1 - SPI INPUT MODE: 1-2 = NON-INVERTED, 2-3 = INVERTED
JP2 - CONNECT V+ TO J19 *
J17/J18 - RELAY SUPPLY (12VDC) [J18 CENTER TIP POSITIVE]
J19 PIN 2 - LOGIC SUPPLY (3.3V - 5V)
D17-D24 = 1N4004
D1-D8 = Red LED (T1-3/4) [RELAY ON]
D9-D16 = GREEN LED (T1-3/4) [INPUT ON]
D26 = Yellow LED (T1-3/4) [RELAY POWER]
D28 = Yellow LED (T1-3/4) [LOGIC POWER]
R25-R32 = 1.5K *
R1_B-R8_B = 560 OHM RESISTOR
ALL RESISTORS ARE 1/4W CARBON
C1 & C2 ARE BYPASS CAPS FOR U10 & U11 RESPECTIVELY
AND SHOULD BE LOCATED AS CLOSE AS POSSIBLE.
ALL TERMINAL BLOCKS 2-POSITION STACKED FOR RELAYS
* SEE DOCUMENTATION



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Digital I/O Board		
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